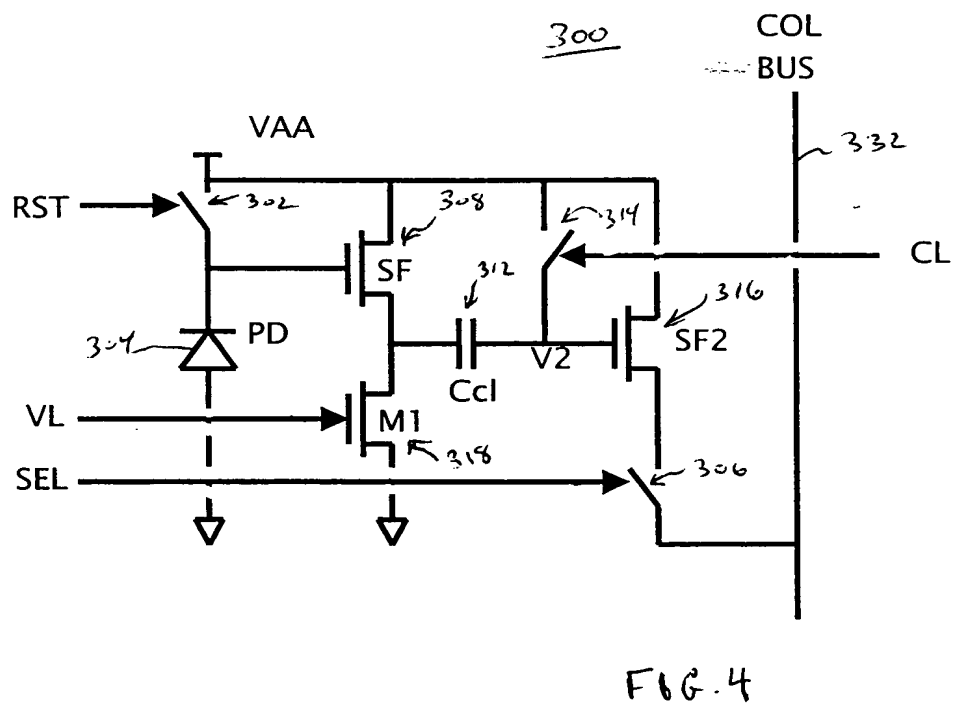
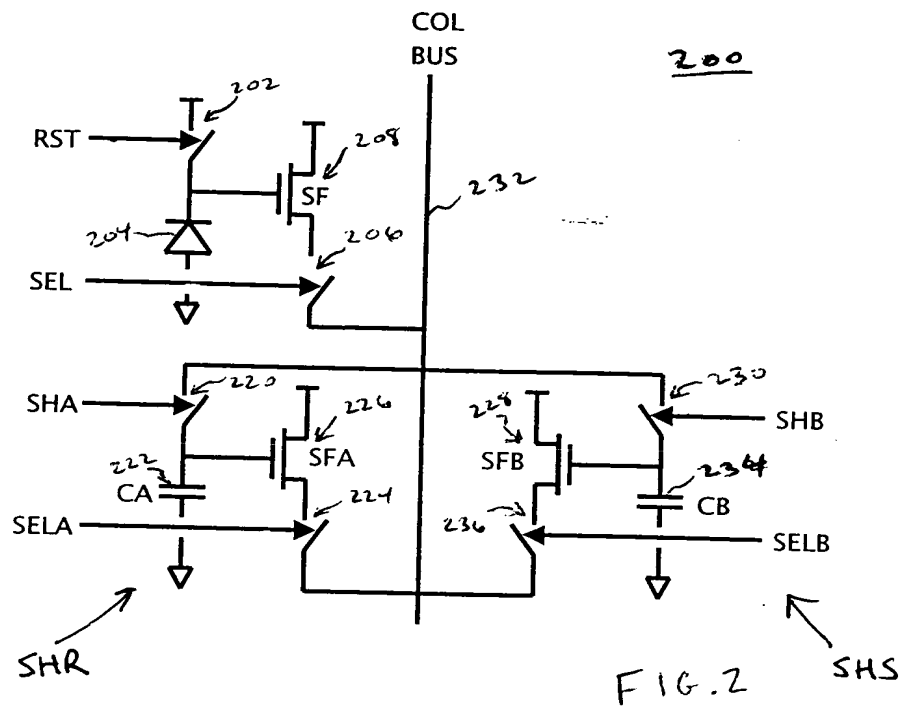




FIG. 1



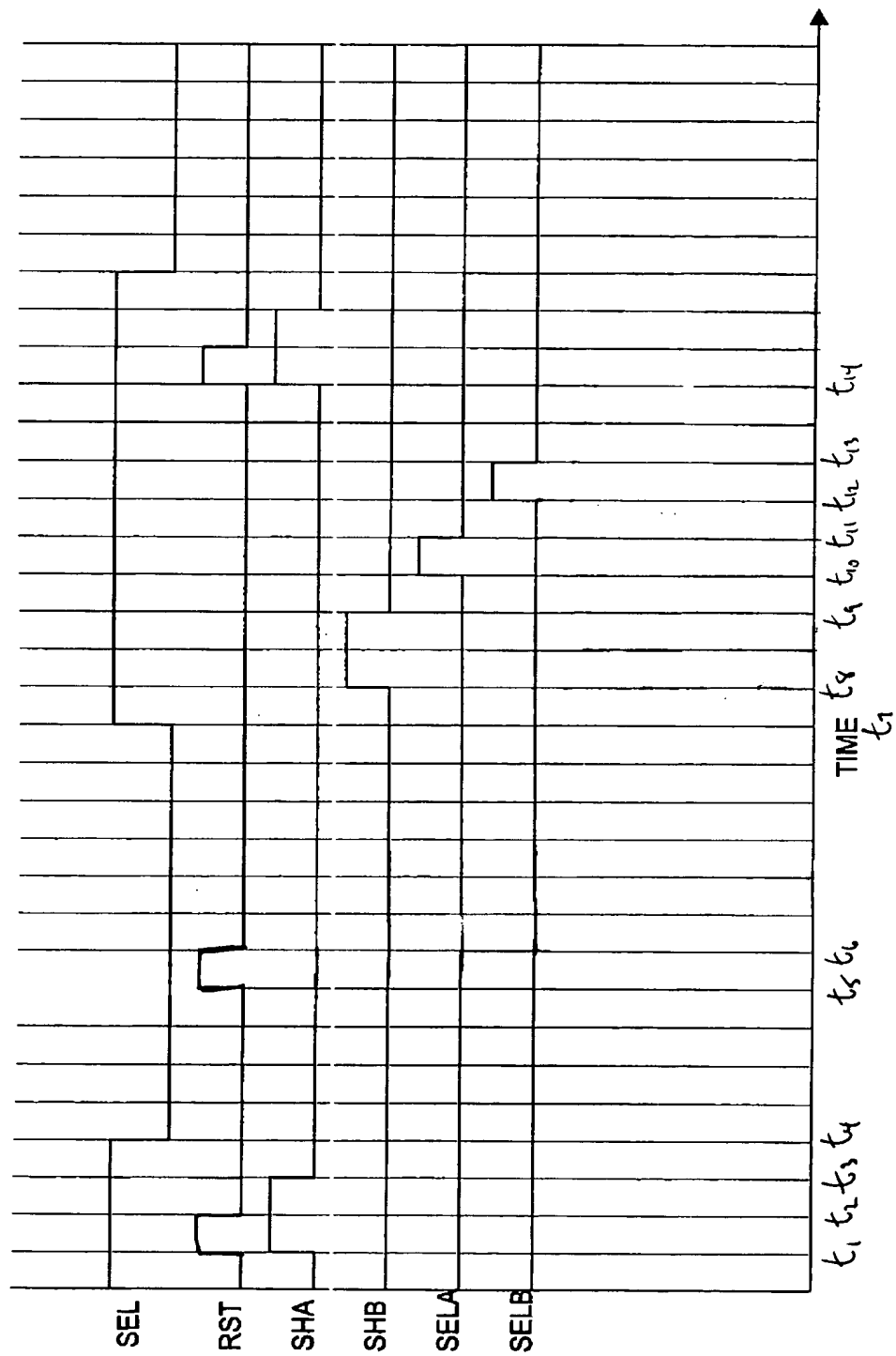


FIG. 3

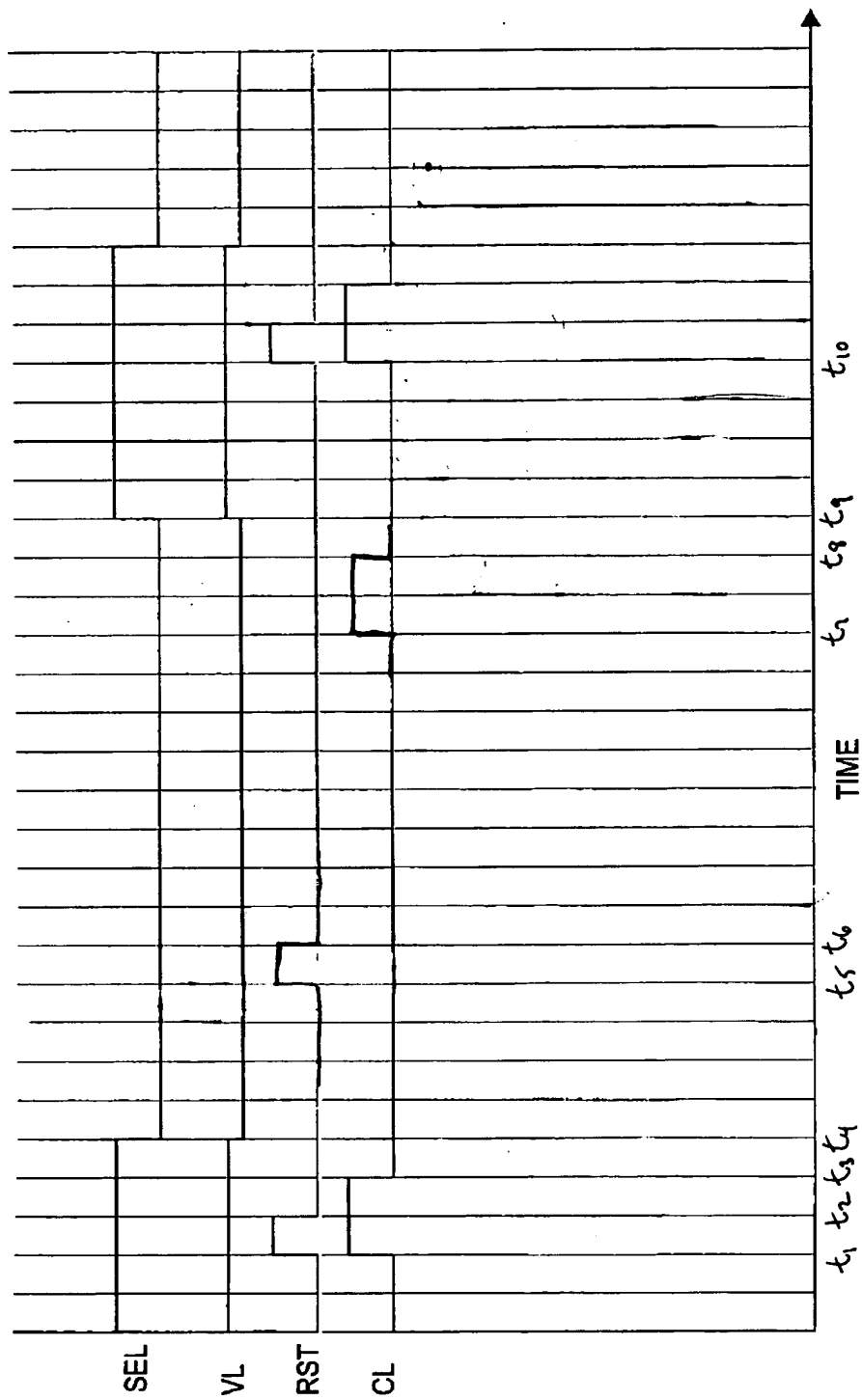


FIG. 5

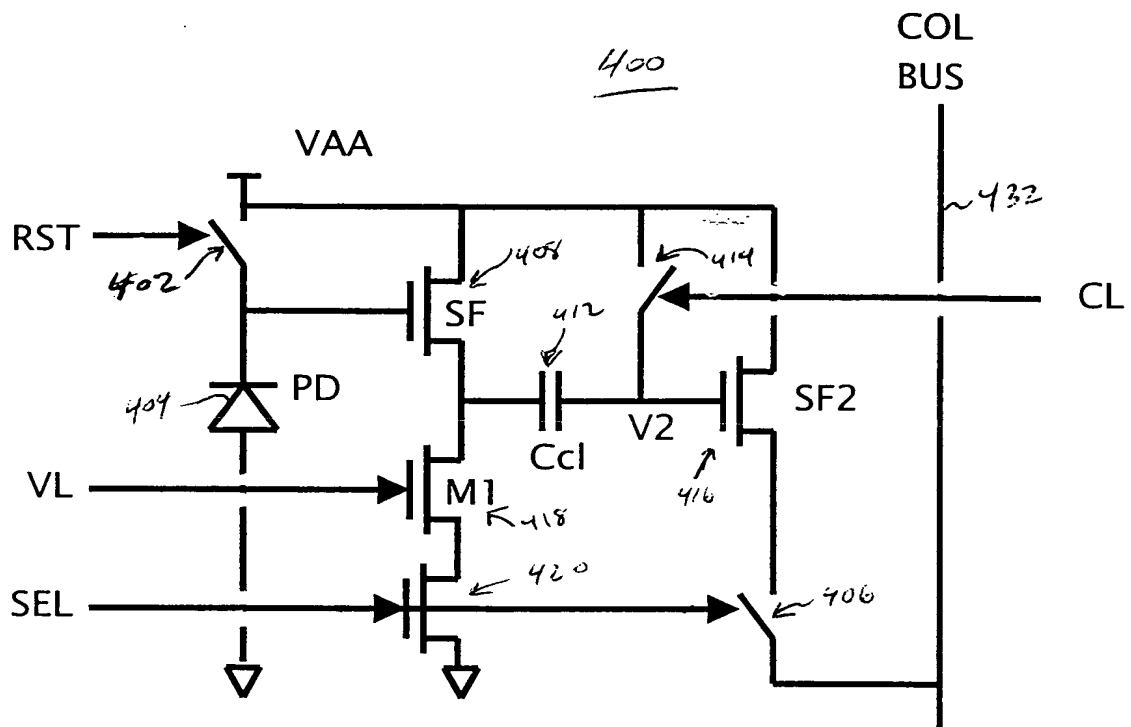


FIG. 6

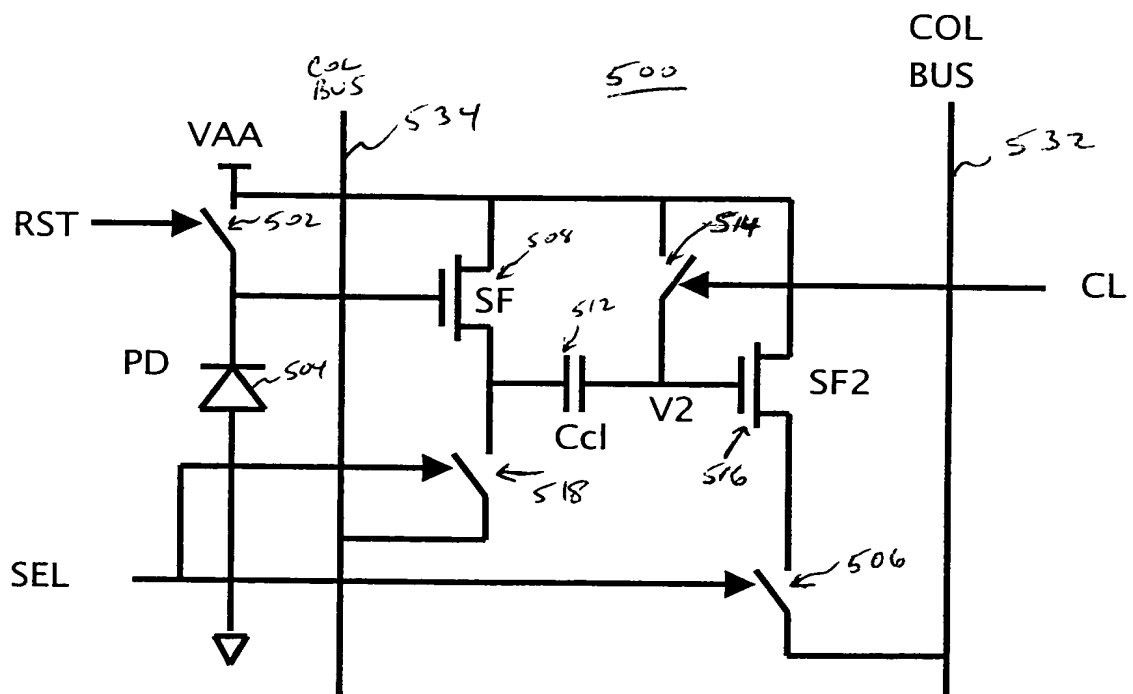


FIG. 8

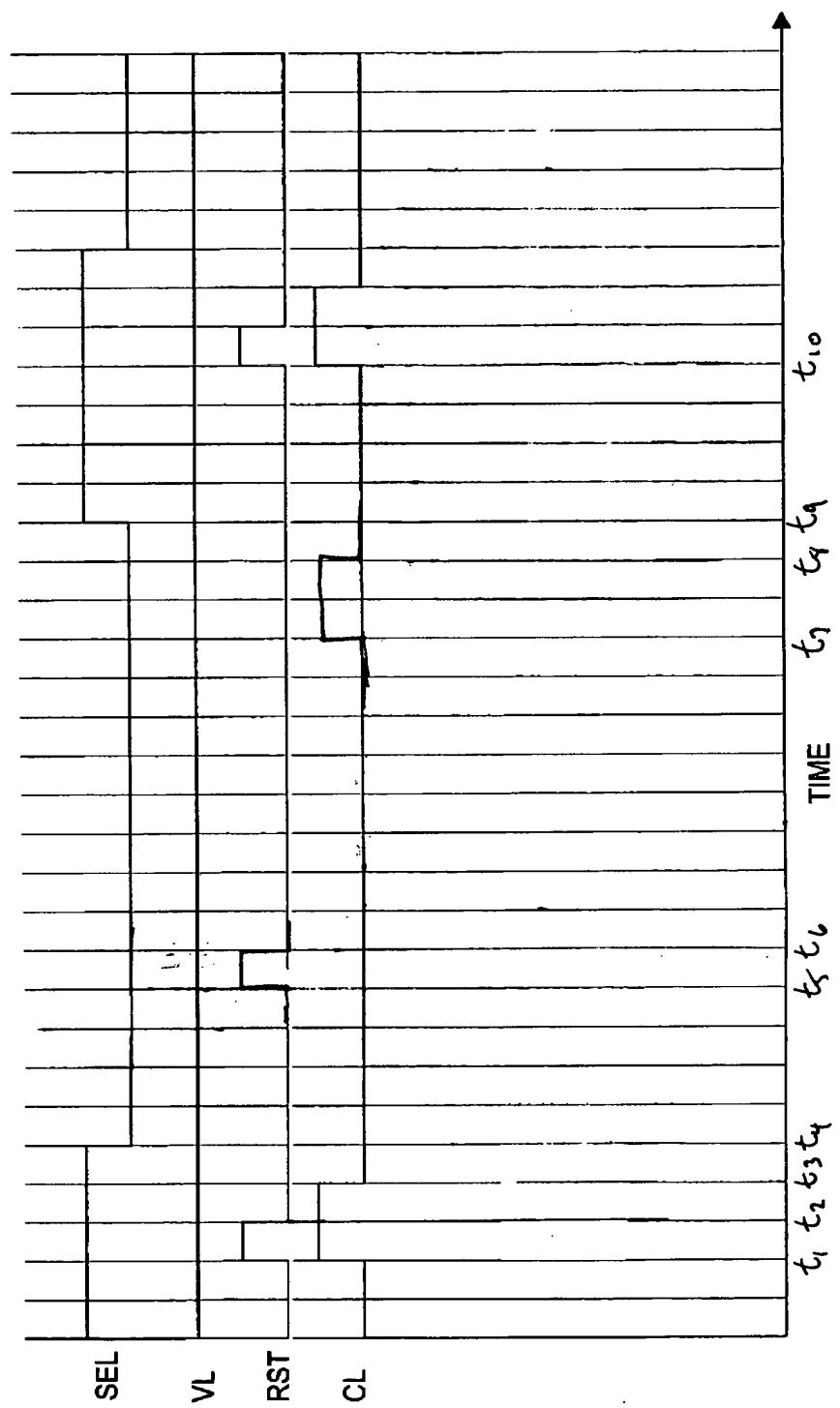


FIG. 7

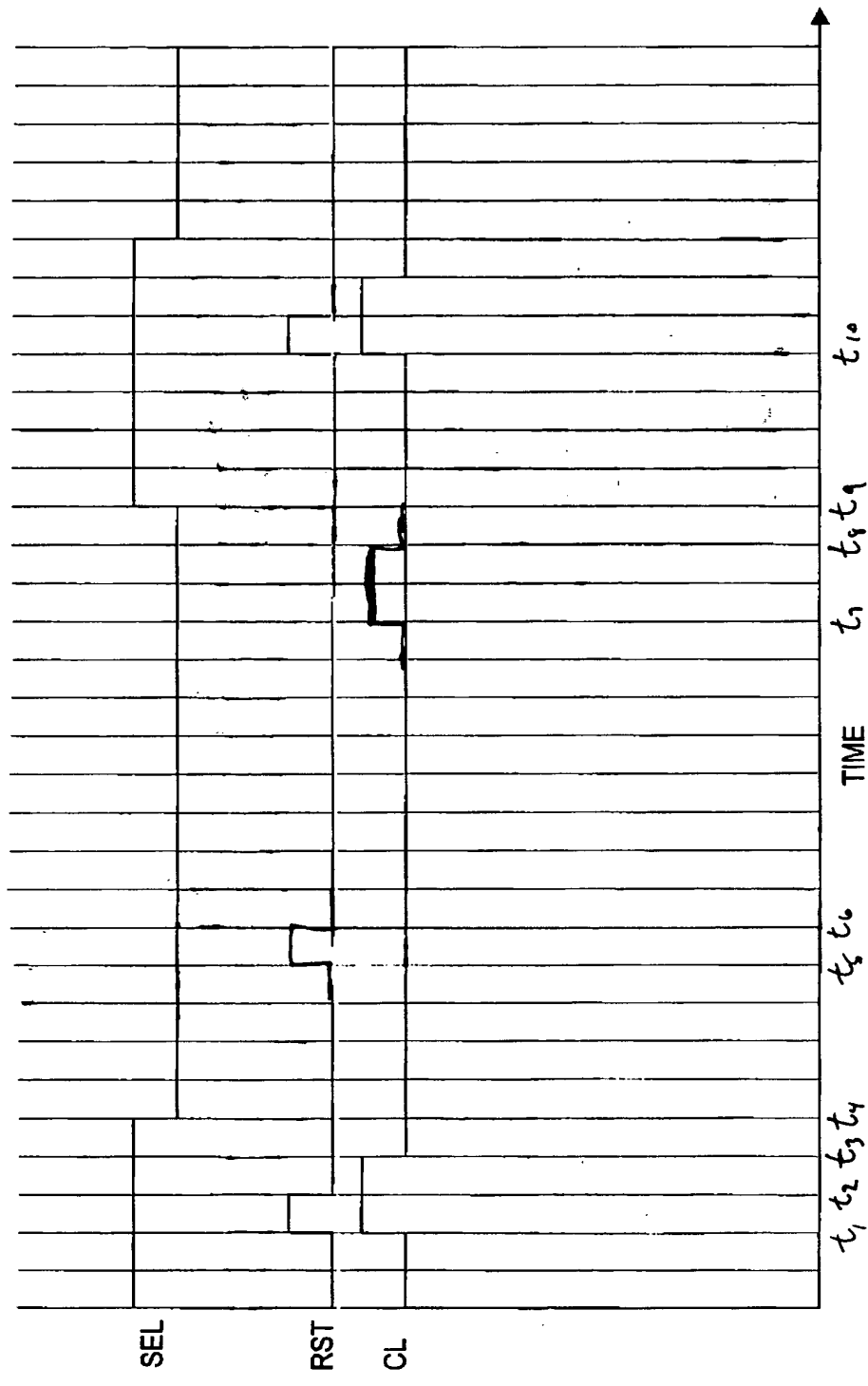


FIG. 9

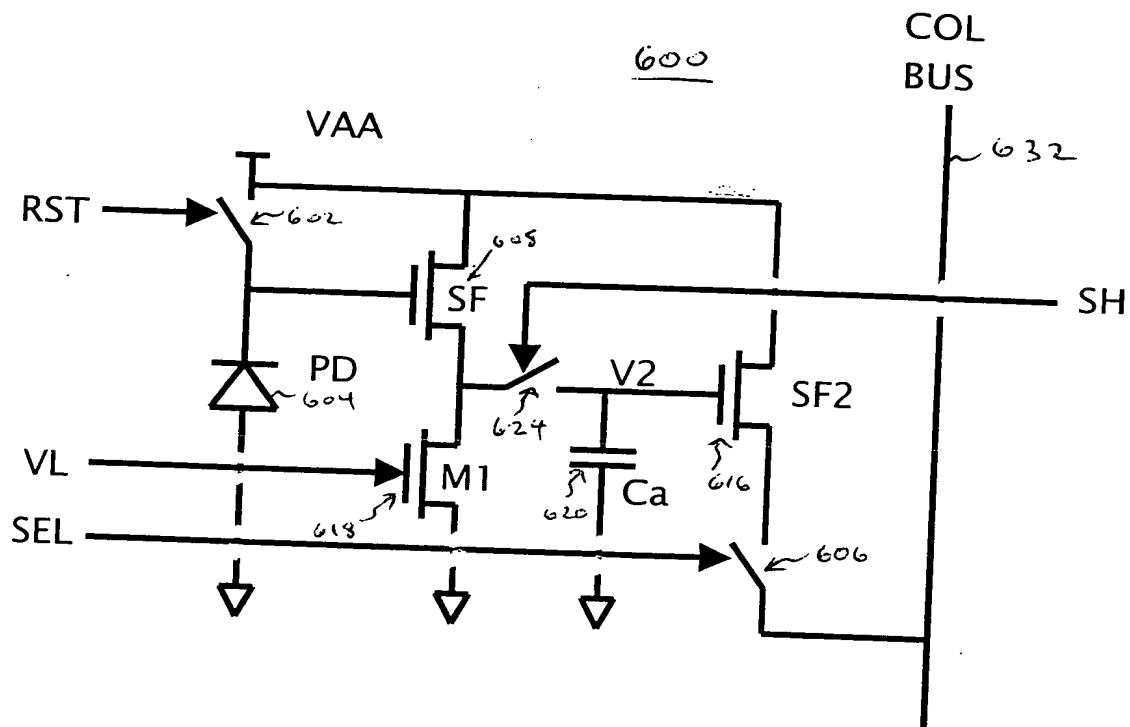


FIG. 10

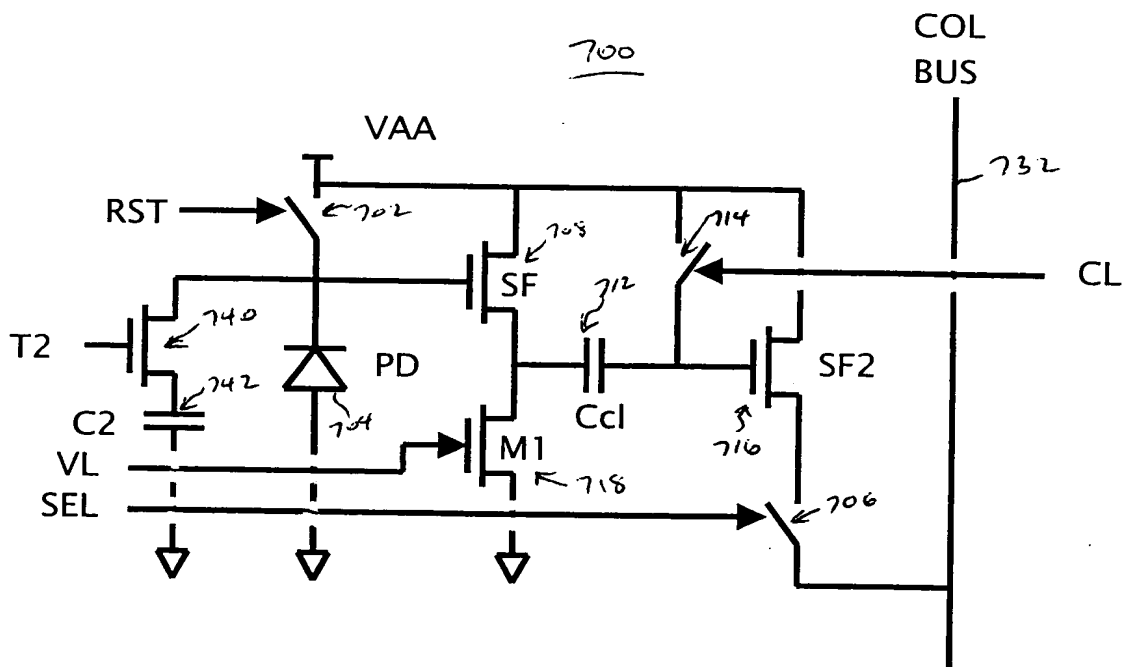


FIG. 12



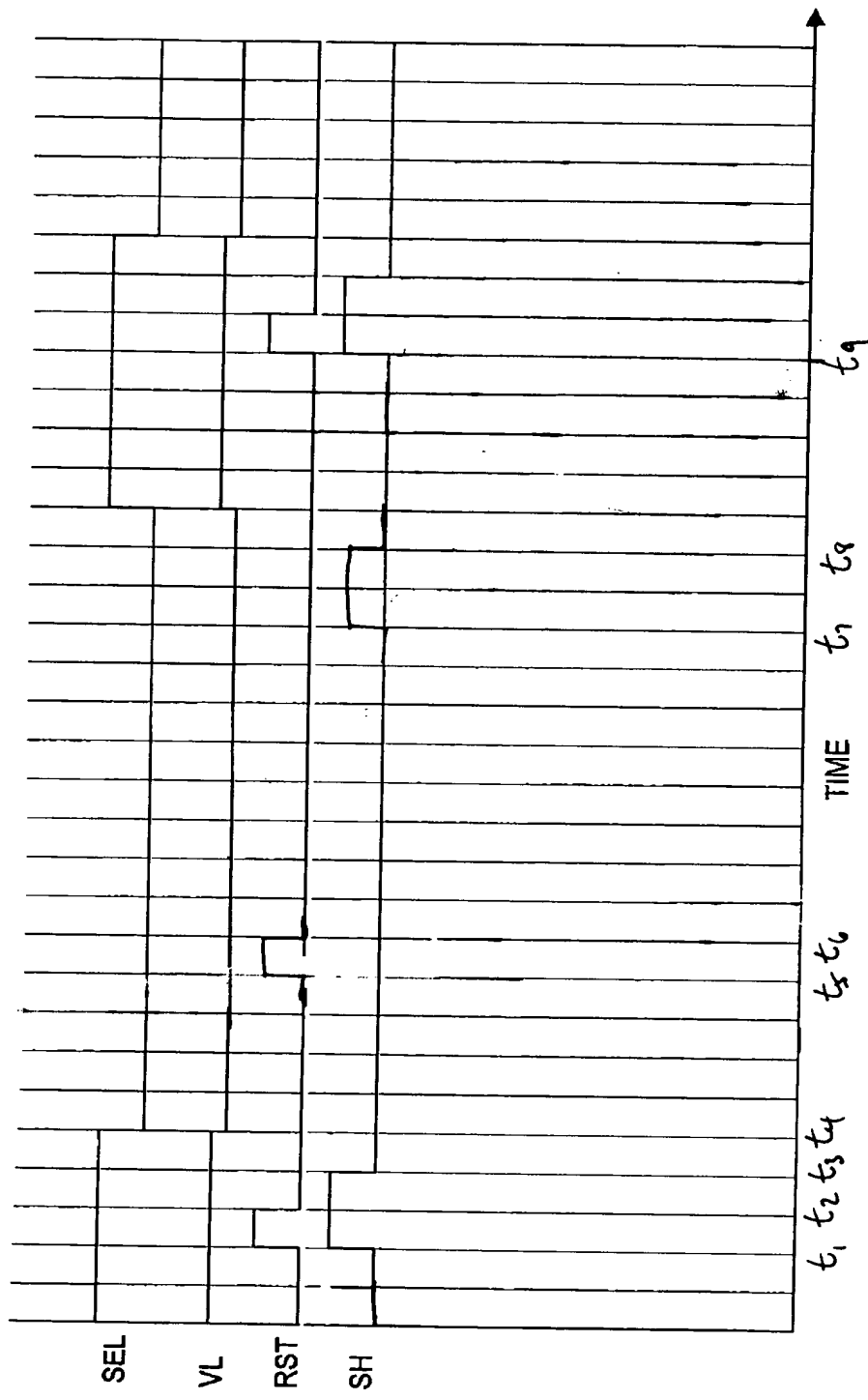
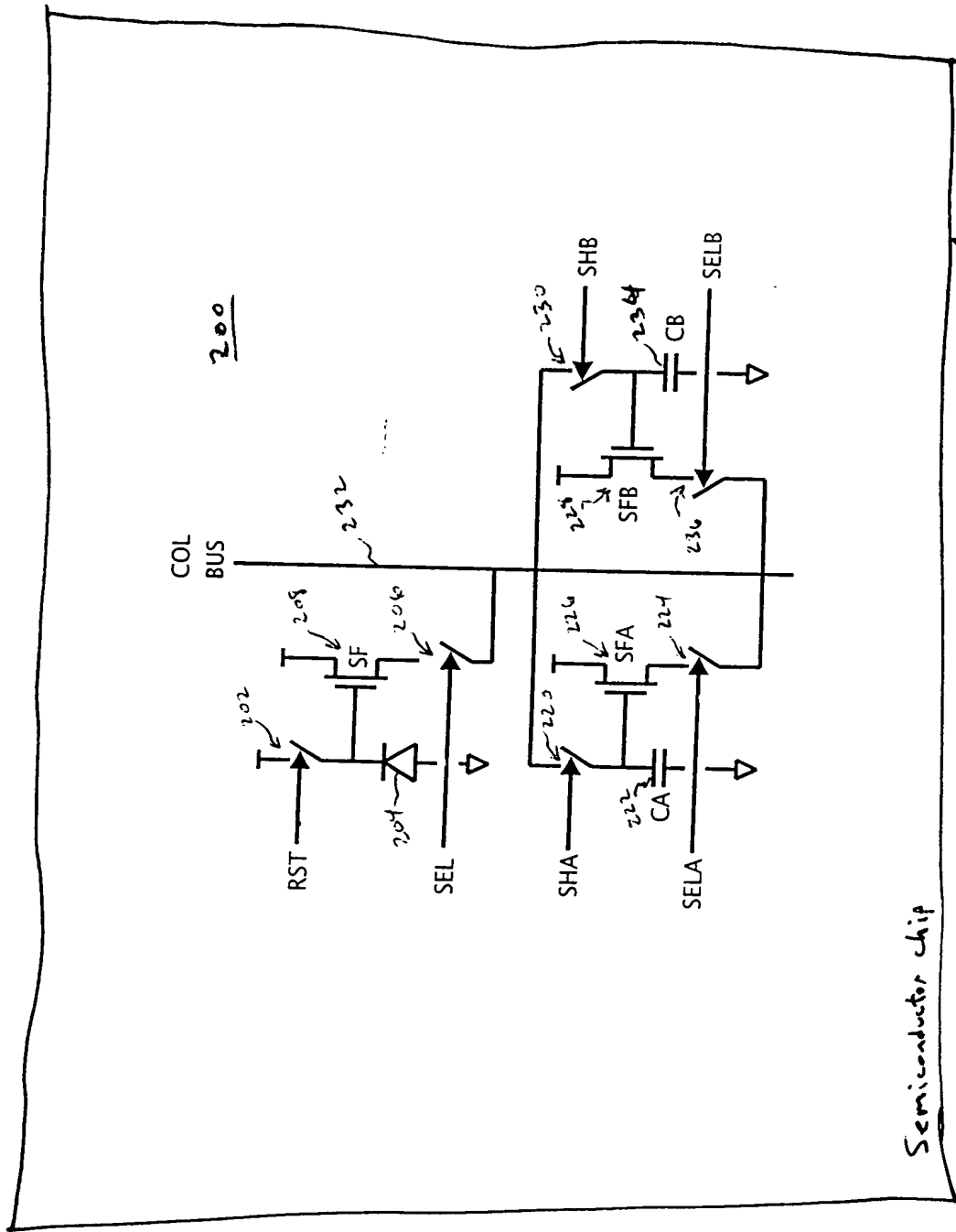


FIG. 11



Semiconductor chip

FIG. 13

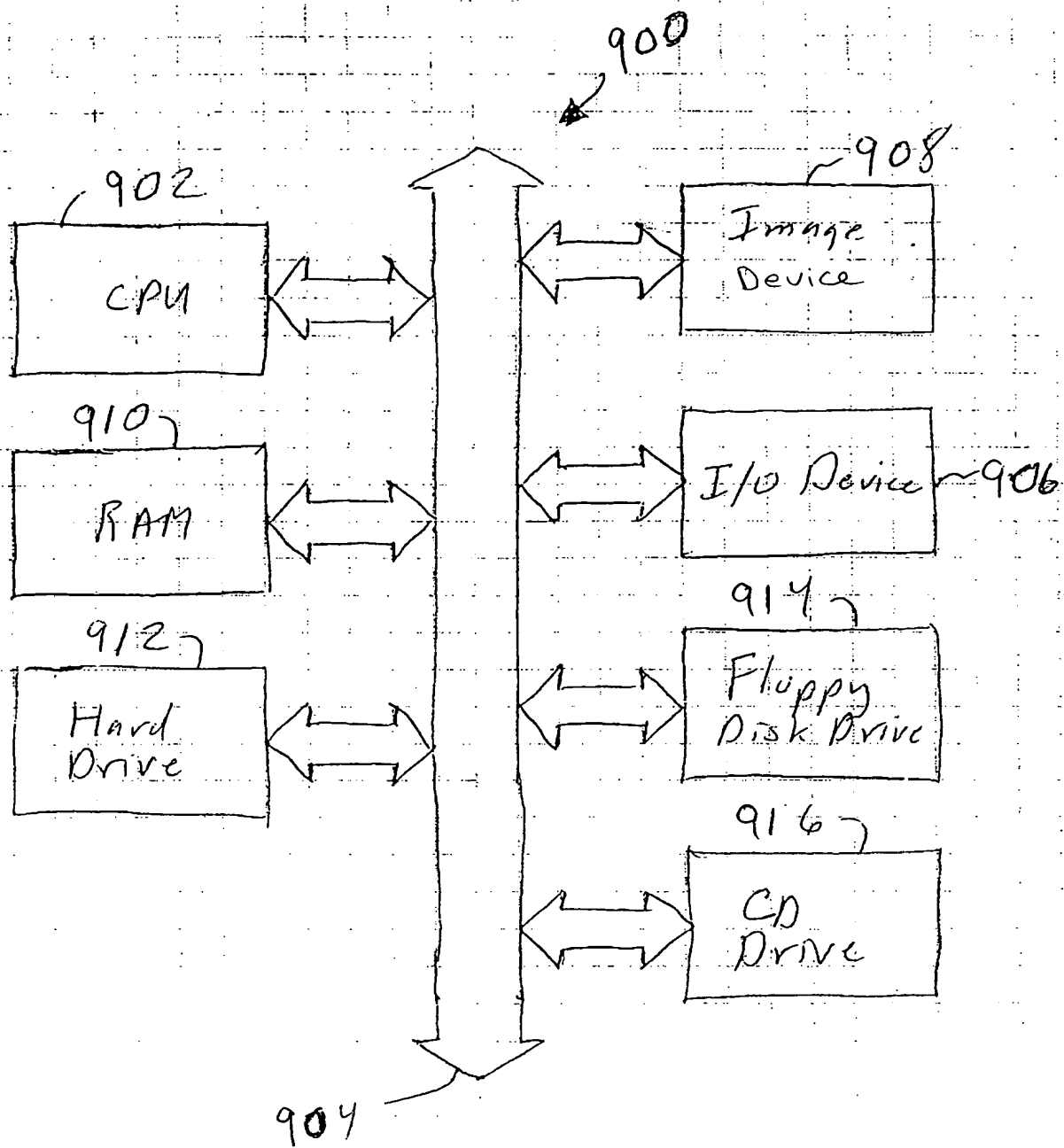


FIG. 14